305-598-2222

Current Global Open Positions in EDA/SEMI & Internet-IOT in R&D, Applications Support, Design and Sales, + as of 7/1/2019...

SR.R&D Developer Verification | Valley Or Taiwan Req #4221
This strong EDA Start-up is in the Verification space and seeks an R&D SW Developer that can also manage a small team.

SW Developer EDA Timing | France #5212
My client is a privately held company based in France and is the worldwide leader of disruptive reprogrammable IPs. Their products allow Integrated Circuits to be reconfigured at will, post-production.

R&D SW Developers... | Germany #5222-23
My client is a leader in their field in Front End Verification tools. This company has a strong international presence with customers Globally and they are, indeed, quite respected in their domain.

R&D SW c++ Algorithmic Developer... | Germany #5224-25
This company is a well-established Global Powerhouse in their domain. They have a terrific core team of engineers and are expanding rapidly.

Director (Manager) Corporate Marketing | Valley...#5229
This really terrific company (and I am not just saying that) are leaders in their domain and are quite established and financially secure. Perhaps even more importantly, they are terrific people with a comfortable working environment and culture.

Application Engineer... FORMAL | Valley and Multiple Locations #5120-1-2
My client is a leader in Verification and simply a great company; they treat their people well. Their software tools help companies to build extraordinarily reliable chips, that help the world's most innovative companies to create bug-free digital integrated circuits.

Application Engineer. GRENOBLE | FRANCE #5218
This established company in Grenoble seeks an experienced FAE that knows RTL Design, Scripting, Verification, Synthesis.

Application Engineer... Timing... | India #5233
This growing Startup has been steadily gaining traction and are expanding their markets. They are in need of an experienced FAE in India that is highly experienced in Timing (STA).

Senior Product Manager...AMS Back End | Valley #5208
My client is a well-funded Startup that has successful working tools with a good customer base. The company has offices in China with a strong presence in the Valley.

R&D S/W Development
EDA/INTERNET-IOT/SEMI +

SR.R&D Developer Verification
Valley Or Taiwan Req #4221

This strong EDA Start-up is in the Verification space and seeks an R&D SW Developer that can also manage a small team. The right candidate must be a Hands-on Strong coder in System Verilog and Strong in UVM. Candidate should have also worked specifically in Memory design-owned the memory controller on the SOC and know DDR3-4-5.

The right engineer could be from the Verification IP space but must be a Senior person that knows engineering process and discipline and is good in front of customers.
Mandarin strongly preferred

SW Developer EDA Timing
France   #5212

My client is a privately held company based in France and is the worldwide leader of disruptive reprogrammable IPs. Their products allow Integrated Circuits to be reconfigured at will, post-production. They are looking for people who want to be part of a company that is paving the next innovations in SoC design and want highly motivated, pro-active and passionate engineers. You will be in charge of the support and the improvement of algorithms in their tool suite, specifically on the timing aspects. You will be actively involved in the Synthesis, Place and Route and Timing engines and in-part, define efficient data structure and algorithm based on requirements. In addition, you will support your work until production, by creating technical specification, implement solution, perform tests and do corrective actions.
- MS or a PhD in Computer Science, Mathematics or related discipline
- At least 5 years of experience in developing applications in C++
- Strong background in math intensive complex algorithms and statistics
- Experience in the EDA synthesis, place and route and timing engines
- Experience in FPGA or ASIC
- Good written and spoken English is mandatory

R&D SW Developers…
Germany #5222-23

My client is a leader in their field in Front End Verification tools. This company has a strong international presence with customers Globally and they are, indeed, quite respected in their domain.
They are seeking SW Developers who know how to apply and understand Provers, BDD's, Solvers, and PDR (Product Directed Reachability) and when to use what methodology. The right candidate should have strong core experience in the Backend and understand the layers above the provers, and how to apply the technology…. PhD preferred but not mandatory.
The right candidate will be experienced c++ SW Developers that understand EDA Design flows, and are eager to work in a fast-paced successful environment. Tcl is essential as well.
They are open to relo for the right dedicated candidate. Terrific benefits…inquire for more details…

R&D SW c++ Algorithmic Developer…
Germany #5224-25

This company is a well-established Global Powerhouse in their domain. They have a terrific core team of engineers and are expanding rapidly.

They are in need of c++ and/or Javascript SW Developers that understand Front-end Verification Development. Knowledge in Debuggers, GUI and UI helpful. Previous experience developing or working with Compilers is also helpful. 2-5 years' experience preferred but smart engineers willing to learn and grow will certainly be considered.

They are open to relo for the right dedicated candidate. Terrific benefits…inquire for more details…

APPLICATION, SUPPORT,
PRODUCT MANAGER & MARKETING

Director (Manager) Corporate Marketing
Valley…#5229

This really terrific company (and I am not just saying that) are leaders in their domain and are quite established and financially secure. Perhaps even more importantly, they are terrific people with a comfortable working environment and culture.

They are in need of an experienced Corporate Marketing Manager/Director with experience in EDA preferred, and a demonstrated history of OUTBOUND Marketing, working in the high technology arena. The right candidate will be required to have a technical understanding of EDA tool flows and possess the ability to articulate the value of the products, to a technical audience.

Additionally, the right candidate will be skilled in Marketing Programs/Management, Strategic Planning, Annual Marketing Plans/Budgets, Corporate Communications, Tradeshows, Public Relations, Digital Marketing, and CRM/Marketing Automation, White Papers, and Marketing Videos.

Generous benefits prevail

Application Engineer… FORMAL
Valley and Multiple Locations #5120-1-2

My client is a leader in Verification and simply a great company; they treat their people well. Their software tools help companies to build extraordinarily reliable chips, that help the world's most innovative companies to create bug-free digital integrated circuits. They are pioneers of advanced formal techniques to solve practical verification challenges. Their focus is on formal verification of chip designs and they provide unique, award winning solutions for tough problems.

Responsibilities:
The right candidate will do Presales and some Post–sales support. They must have strong customer facing skills and own the technical side of the customer relationship, helping the company develop their product. Technically, you will help to enable customers to apply property checking solutions for Formal and Functional Verification of complex modules, IP, processors, and sub-systems, as well as equivalence checking solutions for synthesis and verification for ASIC and FPGA designs.

You will create and perform technical presentations and demonstrations at customer site, and drive and manage technical evaluations.

Requirements

The successful candidate will possess the following combination of education and experience: Master's degree (or PhD) in electrical engineering, microelectronics, computer science or physics and have 4+ years of relevant experience in formal verification of digital circuits, a prerequisite. (Since they have several openings at various levels, experience can vary). Knowledge of verification languages (SVA, PSL), verification methodologies and tools. Working knowledge of HDL such as Verilog, SystemVerilog, and VHDL required.

Strong communication skills to interact with external customers and internal engineering staff. Green card or Citizen required…no H-1's please.

The right candidate will have significant proven relationships with several major well-respected customers and established contacts within.

Have Formal experience with any of the commercial formal tools such as Jasper, OneSpin, Mentor Questa, DC-Formal or VC-Formal

Application Engineer. GRENOBLE FRANCE   #5218

This established company in Grenoble seeks an experienced FAE that knows RTL Design, Scripting, Verification, Synthesis. There is not much travel associated with this position and they are open to relocating candidates (preferably from the EU. They have a good comp package and a strong existing base of customers.

Application Engineer… Timing… India   #5233

This growing Startup has been steadily gaining traction and are expanding their markets. They are in need of an experienced FAE in India that is highly experienced in Timing (STA). The right candidate will know SDC and System Verilog as well.

ACCOUNT MANAGEMENT & SALES

Senior Product Manager…AMS Back End Valley #5208

My client is a well-funded Startup that has successful working tools with a good customer base. The company has offices in China with a strong presence in the Valley.

They are looking for a strong expert in the physical domain arena to work as a Product Manager for their backend Layout (and more) tools. The right candidate will define the product and the requirements of schematic editing, layout editing automation, simulation environment, physical verification (DRC, LVS, LPE) for advanced technology nodes (under 16nm, FinFET process). You will know how to communicate well with both customers and R&D, to identify the product requirements and development goals of their products.

Responsibilities include:

Assist RnD and QA teams, on testing for the product development;

Execute technical support materials by providing all related product documents, such as manuals, and tutorial, and other documents for user training, etc; Develop, maintain, and implement the product roadmap to show differentiated value; Candidates should be from the EDA/Semi world and have experience in using EDA tools for analog circuits, AMS custom, or mixed signal IC design, and familiar with the popular AMS design tools.

Industry experience with standard AMS EDA tools of advanced technology nodes below 16nm (such as, schematic editing, layout editing automation, simulation environment, etc.), as well as experience with Cadence Virtuoso, Virtuoso/Custom Compiler, or Synopsys Laker or similar.

- o   Mandarin and English strongly preferred.

Technology Futures Inc. D/B/A as EDA-CAREERS, is a unique and very different type of Recruitment Company, and that clearly shines through, right from the beginning. With more than 20 years of recruiting exclusively in the EDA/Semi/INTERNET-IOT industry, we pride ourselves on our relationships

Our efforts are unparalleled, when it comes to how we handle our clients and candidates.

Our specialty is in everything EDA/Semi and INTERNET-IOT and we work on and with All Levels of candidates and positions. From VP's to just over Interns, we do it all and we work GLOBALLY.

In this highly technical world of ours, you need a specialized recruiter that understands all elements of the industry...from R&D SW Developers, Application Engineers, Designers, and Sales and Marketing, and more...WE DO IT ALL! AND WE KNOW THE DOMAIN!

At EDA-CAREERS, WE WORK HARD to help our companies find the best candidates, and our candidates, find the best opportunities. We stay involved every step of the way, making sure everyone stays focused throughout the decision making process. In effect, we become PARTNERS so that both the candidate-company hiring process, stays positive and on-track, from beginning to end. That is why we have worked successfully with Hundreds of companies and placed such a broad array of candidates, all around the world. Call us anytime as we work day and night, and advice is always free.

Call us today! 305-598-2222   www.eda-careers.com

Openings are as of this Newsletter date...new openings and closings happen regularly